





Fig. 3(a)

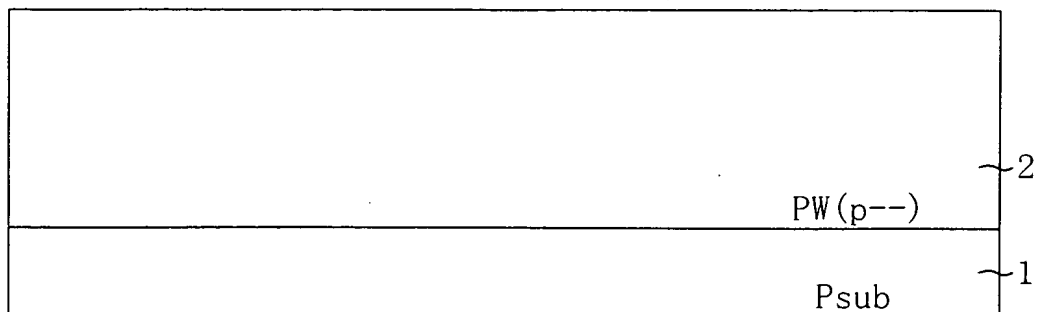


Fig. 3(b)

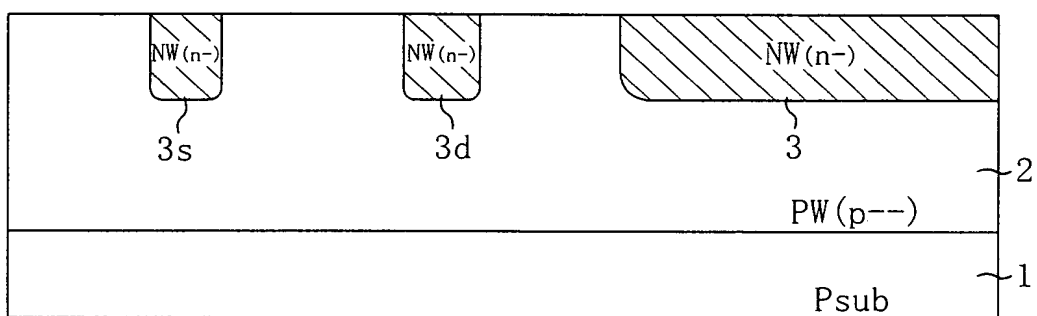


Fig. 3(c)

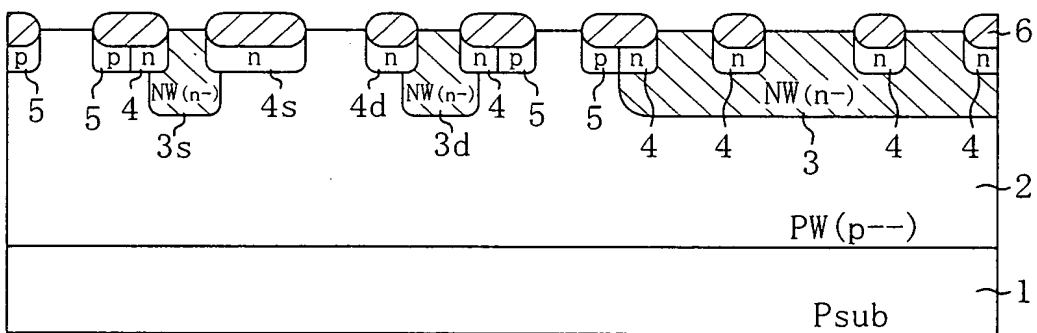


Fig. 3(d)

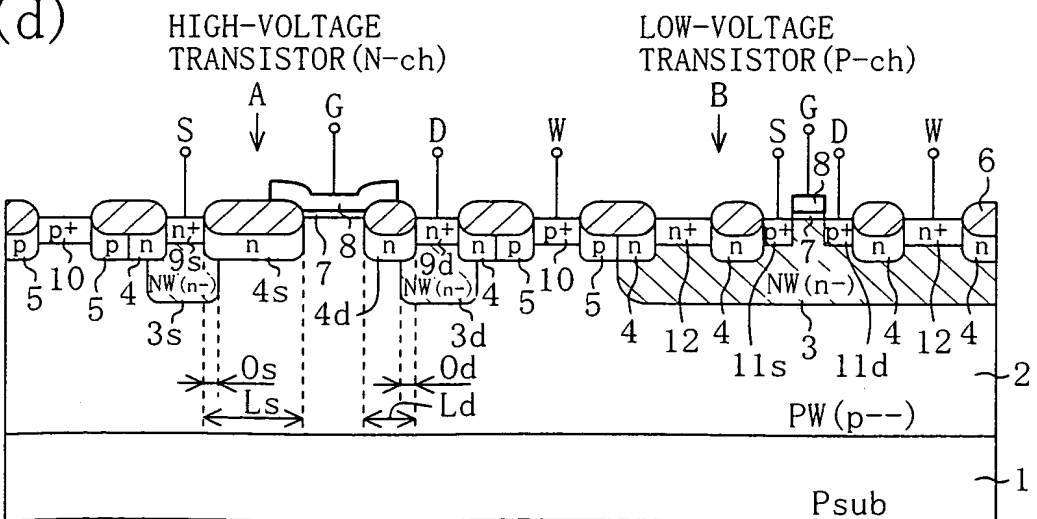




Fig. 5

LOW-VOLTAGE TRANSISTOR (P-ch)

B  
↓  
3(n-)

HIGH-VOLTAGE TRANSISTOR (N-ch)

C  
↓

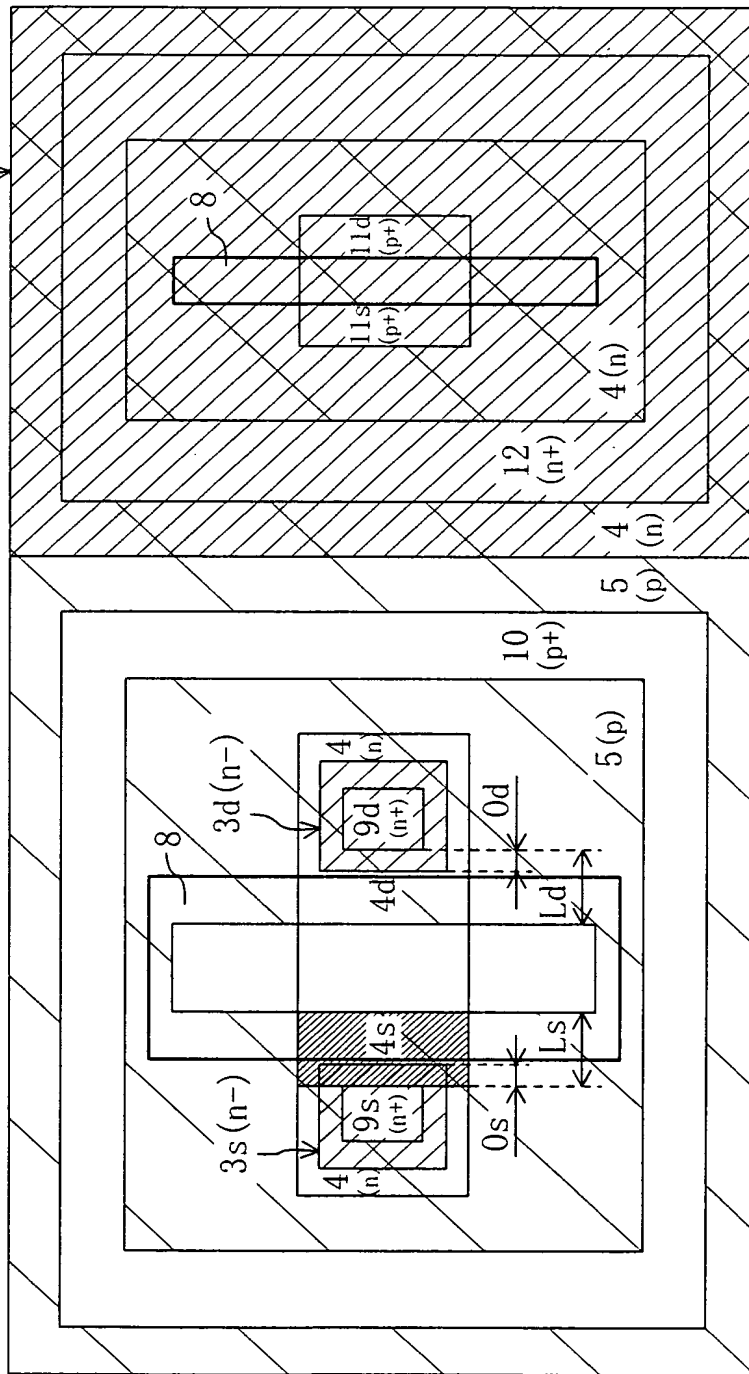


Fig. 6(a)

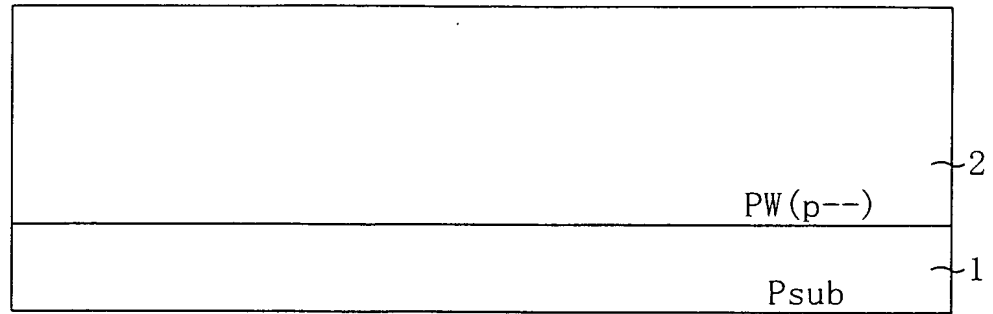


Fig. 6(b)

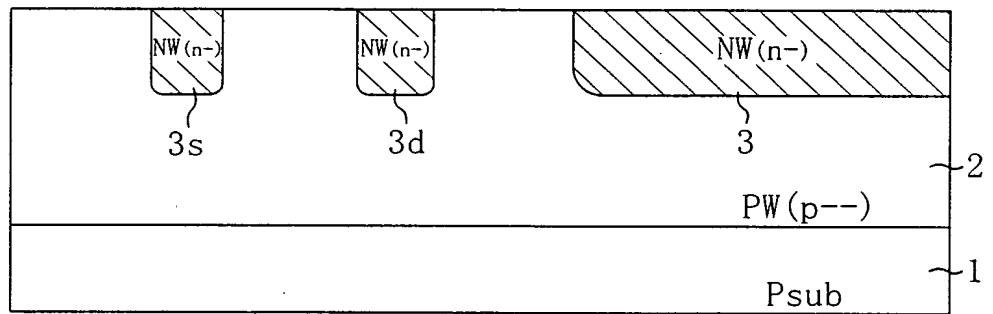


Fig. 6(c)

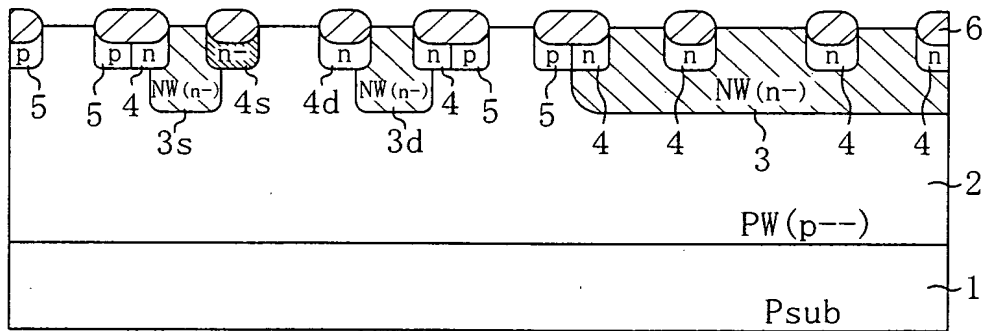
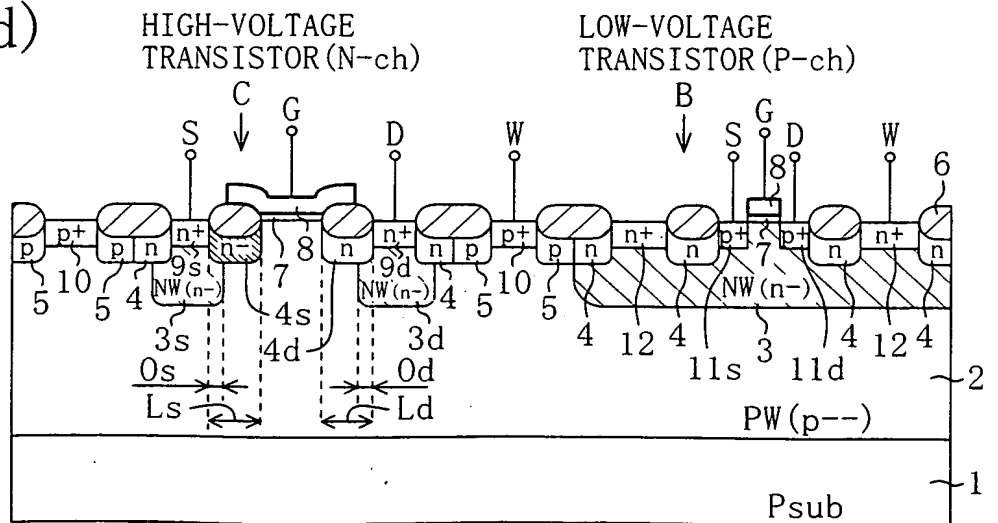
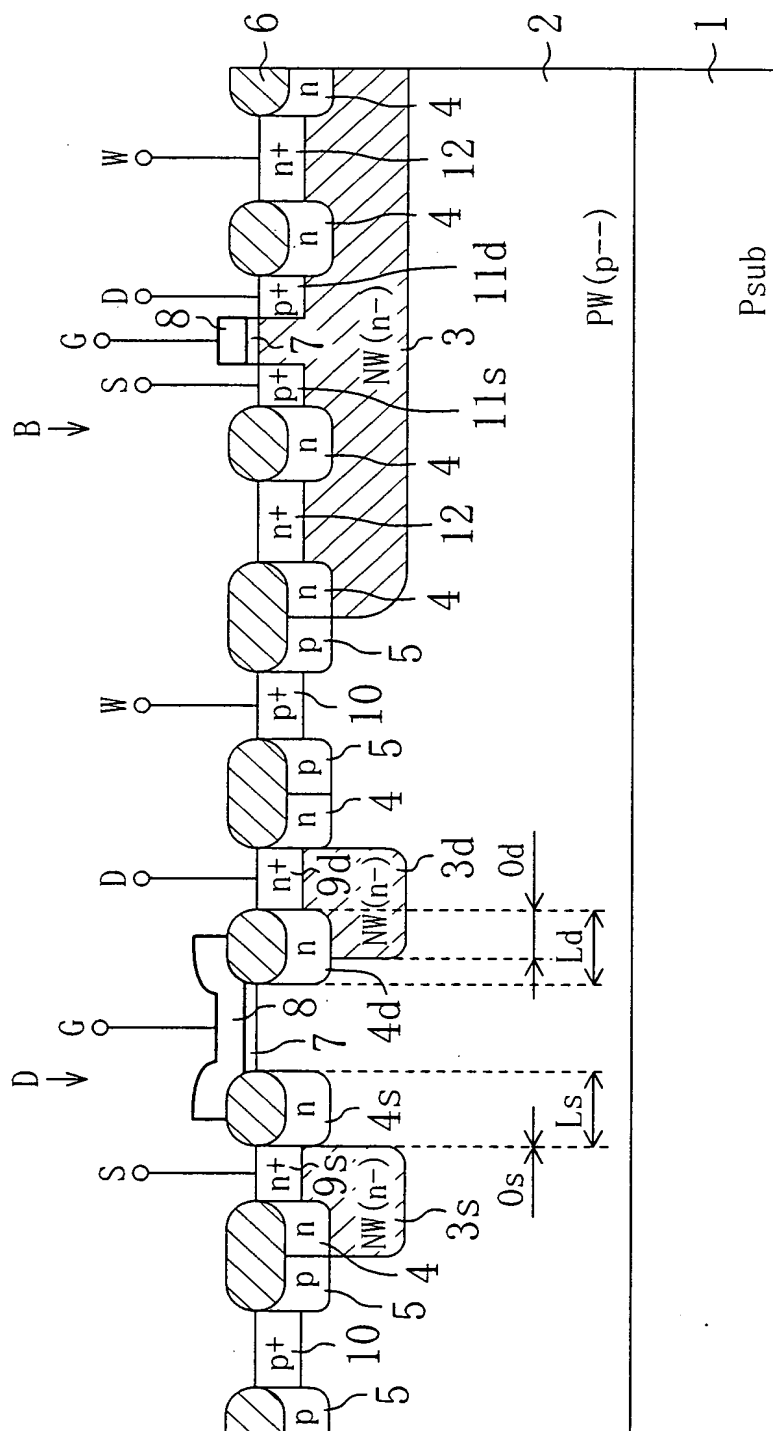


Fig. 6(d)



# LOW-VOLTAGE TRANSISTOR (P-ch)

# HIGH-VOLTAGE TRANSISTOR (N-ch)



000000-000000

Fig. 8

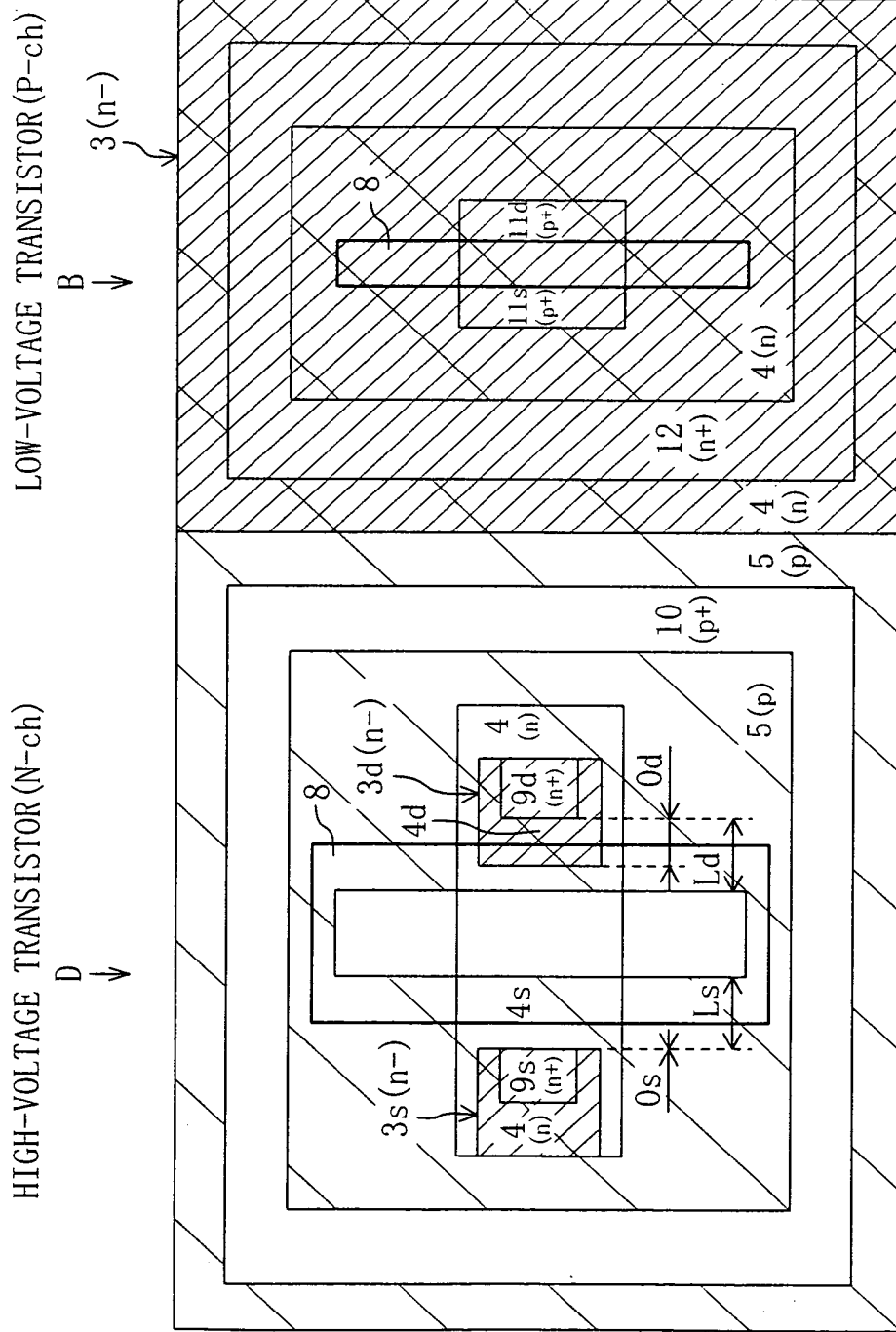




Fig. 9(a)

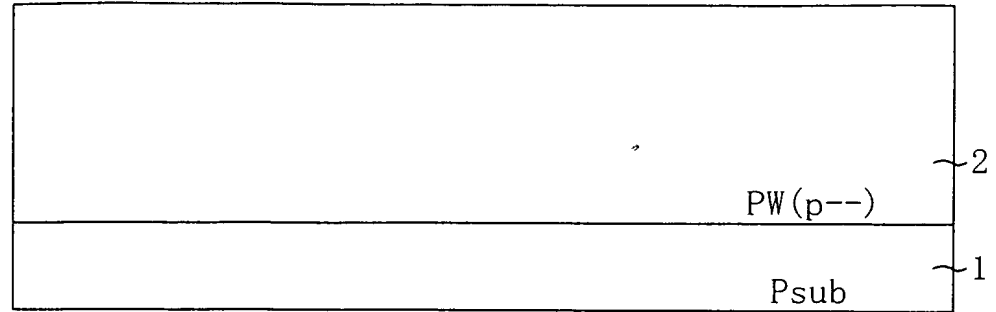


Fig. 9(b)

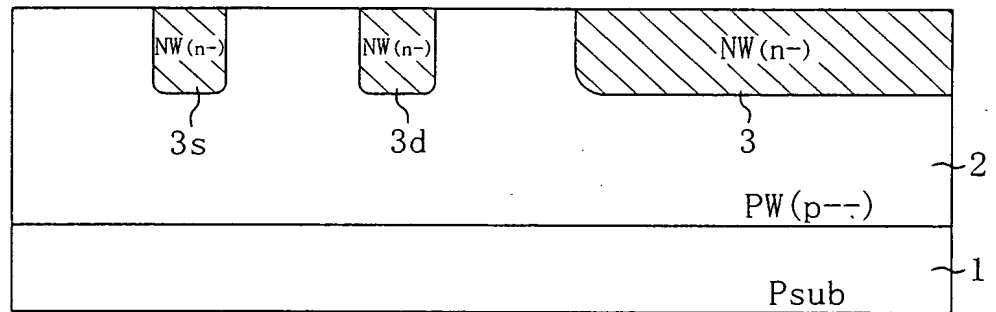


Fig. 9(c)

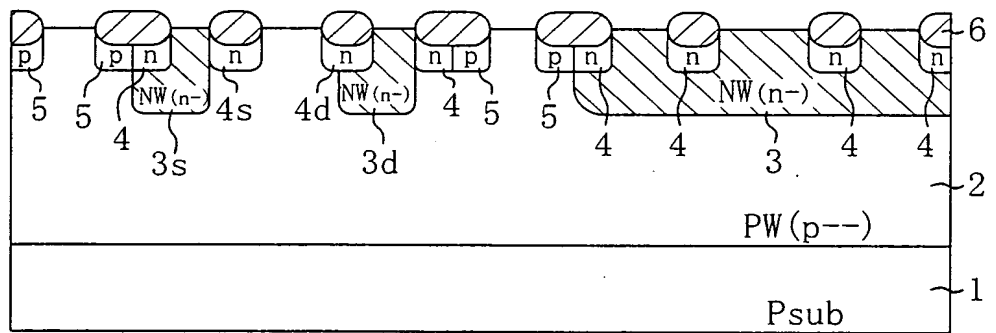
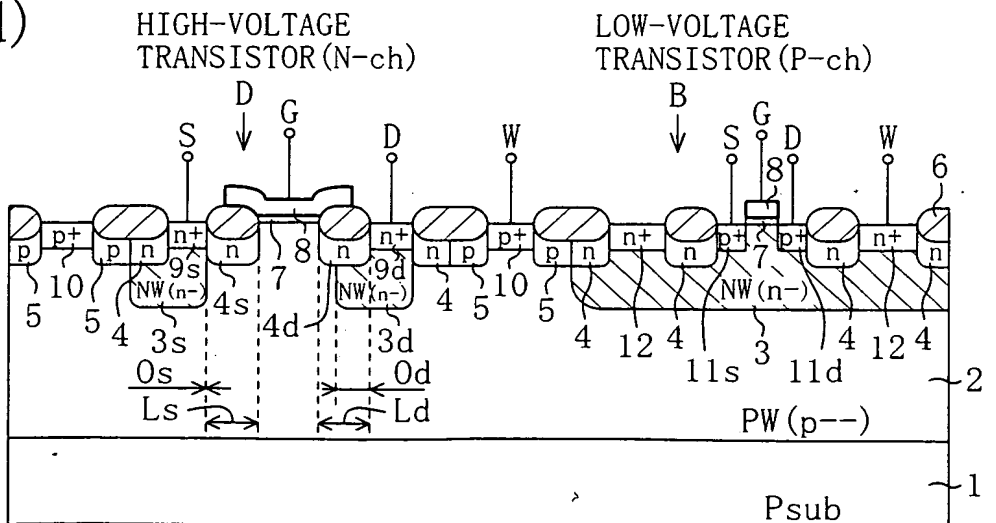


Fig. 9(d)



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Fig. 10 (a)

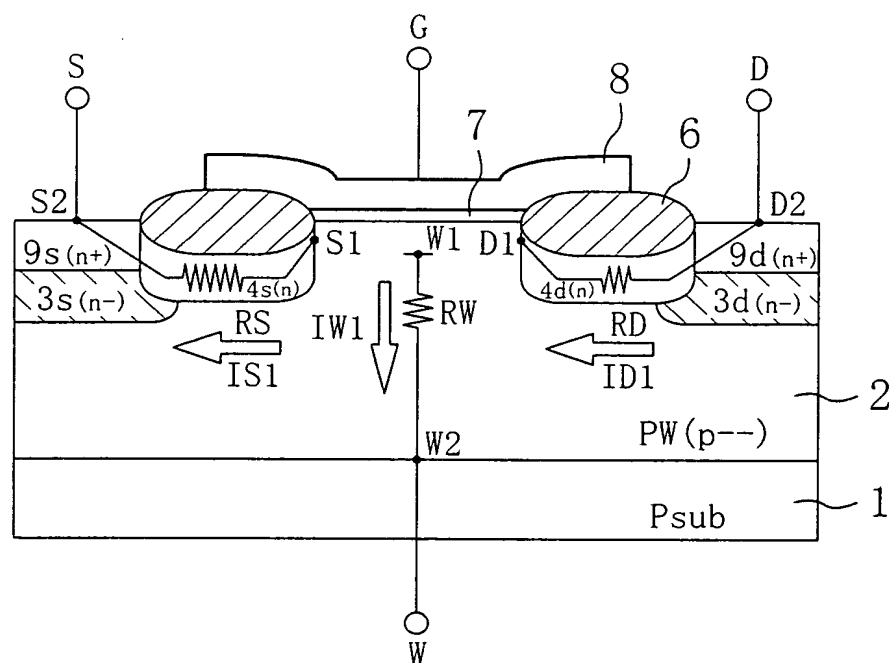


Fig. 10 (b)

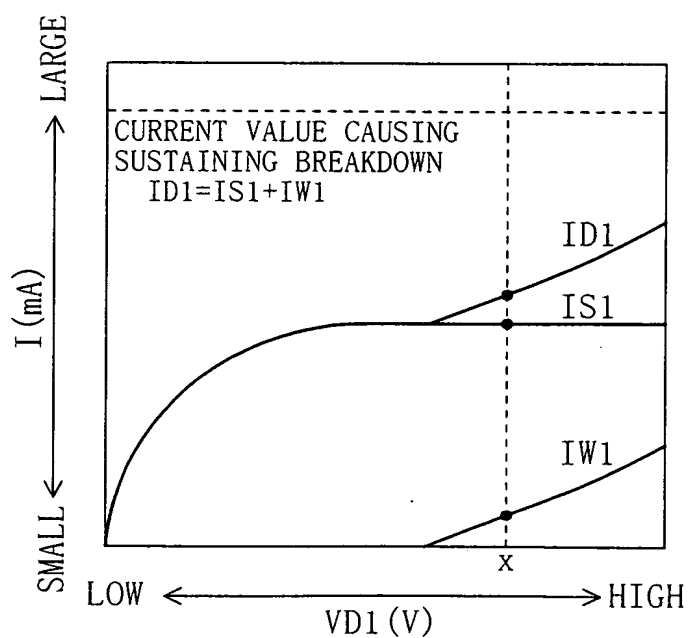


Fig. 11

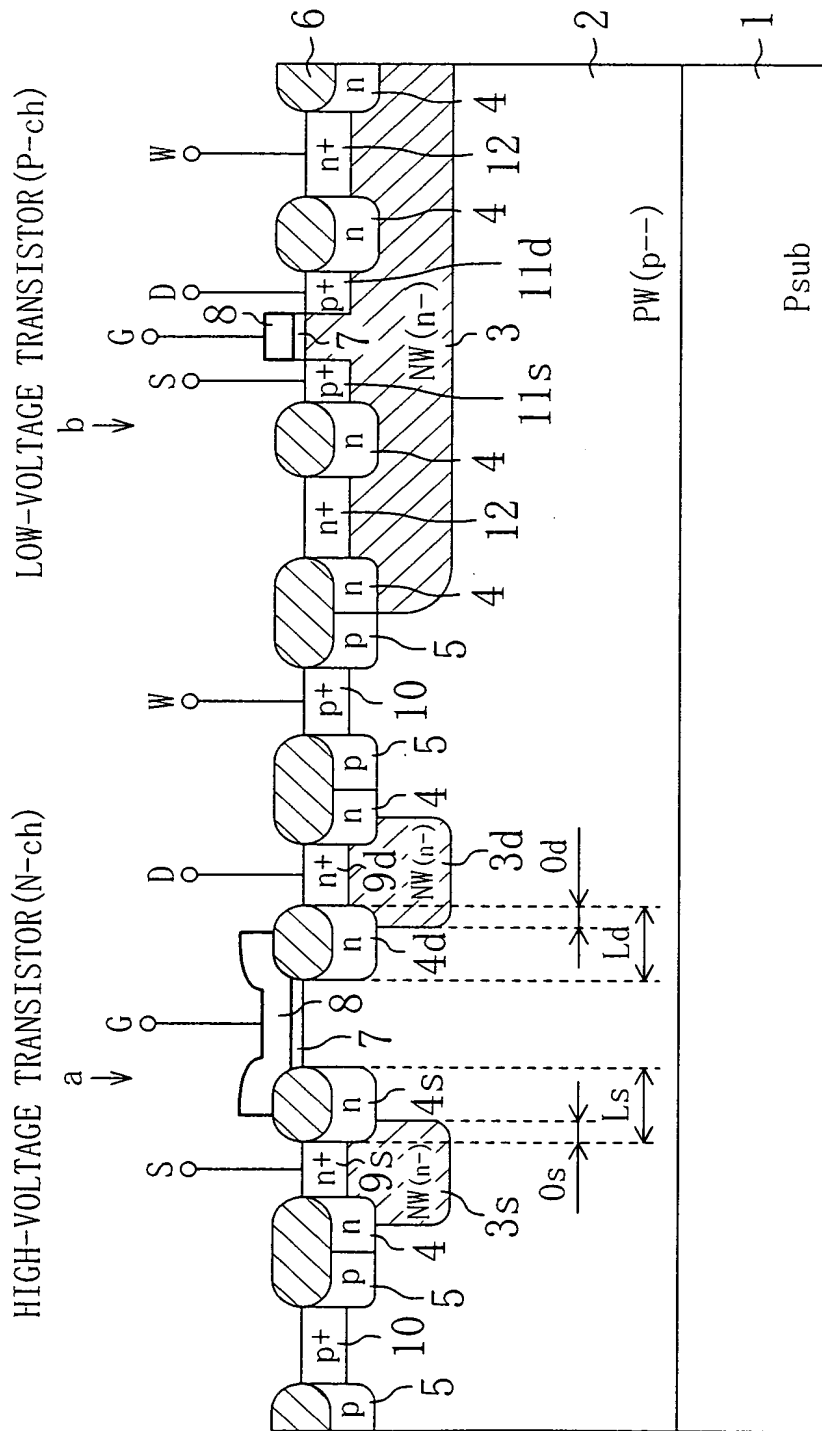


Fig. 12

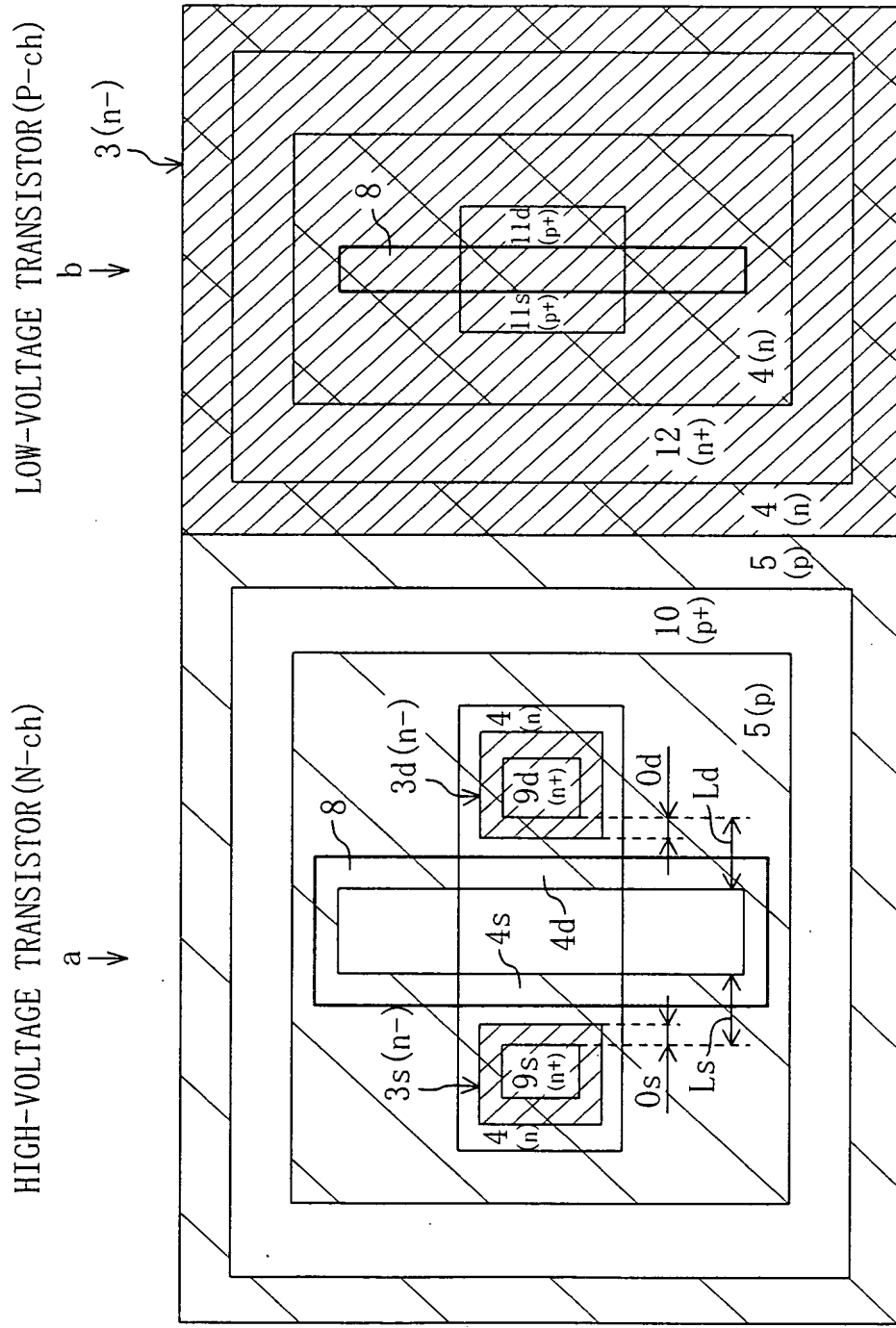


Fig. 13(a)

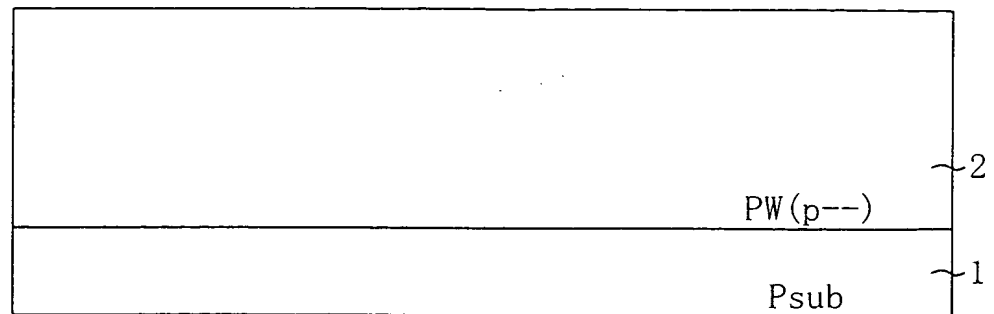


Fig. 13(b)

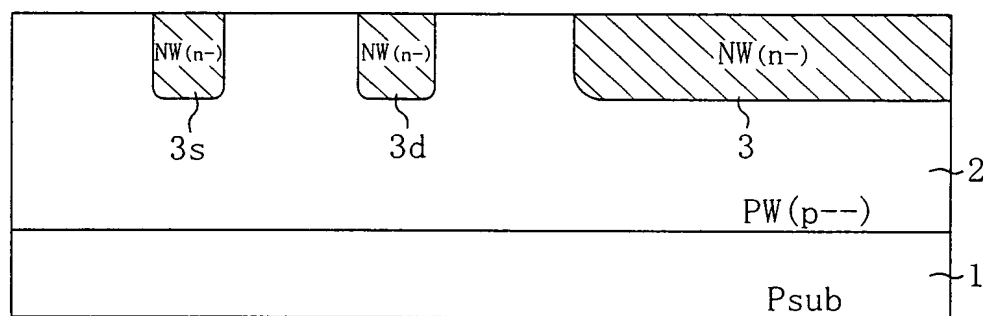


Fig. 13(c)

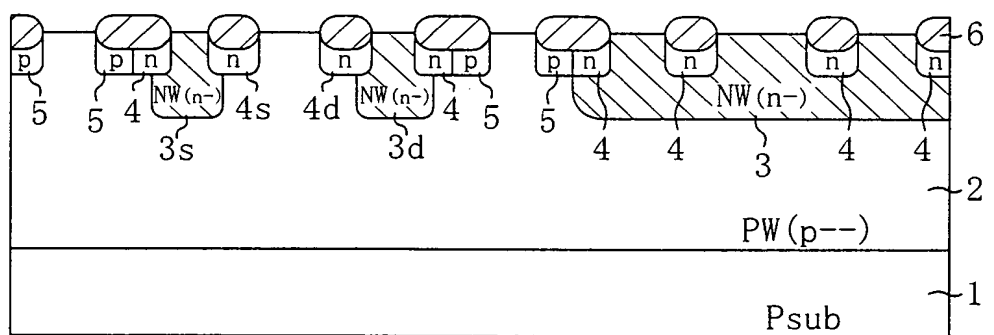
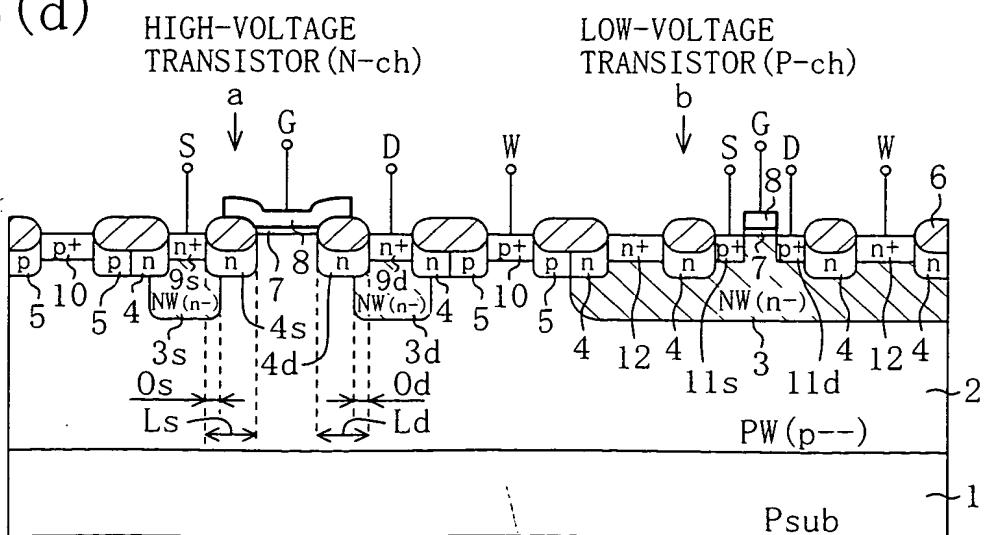
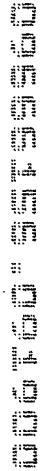


Fig. 13(d)



**PAGE**    **LINE**



**PAGE 00000000**

